

# NOISE-PROOF BUS CIRCUIT

## Abstract

OLE\_LINK1A noise-free bus circuit for diminishing noises of an original clock signal over a bus. The noise-free bus circuit has a connection wire module and a voltage detection circuit. The connection wire module includes the bus and a conduction wire disposed along the bus. The bus has a first end connected to the original clock signal while the conduction wire has a first end connected to a reference voltage. The voltage detection circuit is electrically connected to second ends of the bus and the conduction wire for generating an amended clock signal by determining a voltage difference between voltages at the second ends of the bus and of the conduction wire, the amended clock signal being equivalently equal to the original clock signal without the noises.